

AMENDMENTS TO THE CLAIMS

1. (Original) An interposer-forming clad plate for use in a semiconductor device manufactured by press-bonding a copper foil material and a nickel foil material at a rolling reduction of 0.1 to 3%.

C8 2. (Original) An interposer-forming clad plate for use in a semiconductor device manufactured by press-bonding a copper foil material having nickel plating on one surface or both surfaces and other copper foil material or a copper foil material having nickel plating on one surface at a rolling reduction of 0.1 to 3%.

3. (Original) A clad plate as defined in claim 1 or 2, wherein the clad plate comprises five layers of copper/nickel/copper/nickel/copper.

4. (Previously Amended) An interposer for use in a semiconductor device in which a clad plate as defined in any one of claims 1 or 2 is etched selectively to form connecting bumps with a semiconductor chip and a wiring layer, the semiconductor chip and the wiring layer are connected by way of the semiconductor chip connection bumps using anisotropically conductive adhesives and conduction of the

interposer in the direction of the thickness is taken by way of a columnar conductor formed by etching..

5. (Currently Amended) A method of manufacturing an interposer for use in a semiconductor device, which comprises laminating a copper foil material to form a conductor layer ~~or the like~~ and a nickel foil or nickel plating to form an etching stop layer, press-bonding them at a rolling reduction of 0.1 to 3% to form an interposer-forming clad layer for use in a semiconductor device, selectively etching the clad plate to form a columnar conductor, forming an insulation layer on the copper foil material to form a wiring layer, and forming a semiconductor chip connection bumps and the wiring layer to the clad plate on the side opposite to the surface for forming the columnar conductor.

6. (Currently Amended) A method of manufacturing an interposer-forming clad layer for use in a semiconductor device as defined in any one of claims 1 or 2 wherein the interposer-forming clad plate for use in the semiconductor device is formed by previously applying an activating treatment to the bonded surfaces of the copper foil and the nickel foil or nickel plating in a vacuum vessel and then laminating the copper foil and the nickel foil material or nickel plating and cold press-bonding them at a rolling reduction of 0.1 to 3% in which the activating treatment is

Cont. applied $\langle 1 \rangle$ in an inert gas atmosphere at an extremely low pressure of 1×10^1 to 1×10^{-2} Pa, $\langle 2 \rangle$ using the nickel plated copper foil material and the copper foil material as one electrode A having the bonding surfaces grounded to the earth, respectively, and conducting glow discharge by applying an AC current at 1 to 50 MHz between it and the other electrode B supported insulatively and $\langle 4 \rangle$ applying sputter etching, $\langle 3 \rangle$ with the area of the electrode exposed in plasmas caused by the glow discharge being $1/3$ or less of the electrode B.

7. (Currently Amended) A method of manufacturing an interposer-forming clad layer for use in a semiconductor device as defined in claim 3 wherein the interposer-forming clad plate for use in the semiconductor device is formed by previously applying an activating treatment to the bonded surfaces of the copper foil and the nickel foil or nickel plating in a vacuum vessel and then laminating the copper foil and the nickel foil material or nickel plating and cold press-bonding them at a rolling reduction of 0.1 to 3% in which the activating treatment is applied $\langle 1 \rangle$ in an inert gas atmosphere at an extremely low pressure of 1×10^1 to 1×10^{-2} Pa, $\langle 2 \rangle$ using the nickel plated copper foil material and the copper foil material as one electrode A having the bonding surfaces grounded to the earth, respectively, and conducting glow discharge by applying an AC current at 1 to 50 MHz between it

and the other electrode B supported insulatively and <4>
applying sputter etching, <3> with the area of the electrode
exposed in plasmas caused by the glow discharge being 1/3 or
less of the electrode B.

8. (Previously Added) An interposer for use in a
semiconductor device in which a clad plate as defined in claim
3 is etched selectively to form connecting bumps with a
semiconductor chip and a wiring layer, the semiconductor chip
and the wiring layer are connected by way of the semiconductor
chip connection bumps using anisotropically conductive
adhesives and conduction of the interposer in the direction of
the thickness is taken by way of a columnar conductor formed
by etching.
